

REISSUE APPLICATION DECLARATION BY THE ASSIGNEE		Docket Number: 740819-1052
I hereby declare that:		
The residence, mailing address and citizenship of the inventors are stated below.		
I am authorized to act on behalf of the following assignee: <u>Matsushita Electric Industrial Co., Ltd.</u>		
and the title of my position with said assignee is: _____		
The entire title to the patent identified below is vested in said assignee.		
Inventor: Yoshihisa NAGANO	Citizenship: JAPAN	
Residence/Mailing Address: 3-26-1-203, Hara-cho, Saita-shi, Osaka 564-0004 JAPAN		
Inventor: Yasuhiro UEMOTO	Citizenship: JAPAN	
Residence/Mailing Address: 2-2-31, Nakagoshio, Otsu-shi, Shiga 520-0837 JAPAN		
<input type="checkbox"/> Additional inventors are named on separately numbered sheets attached hereto.		
Patent Number: 6,441,420	Date of Patent Issued: August 27, 2002	
Title of Invention: <u>SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME</u>		
I believe said inventor(s) to be the original and first inventor(s) of the subject matter which is described and claimed in said patent, for which a reissue patent is sought on the invention entitled <u>SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME</u> , the specification of which		
<input type="checkbox"/> is attached hereto. <input checked="" type="checkbox"/> was filed on April 22, 2004 as reissue application number 10/829,476.		
I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.		
I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.		
I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)		
<input type="checkbox"/> by reason of a defective specification or drawing. <input checked="" type="checkbox"/> by reason of the patentee claiming more or less than he had the right to claim in the patent. <input type="checkbox"/> by reason of other errors.		

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At least one error upon which reissue is based is described as follows:

The recitation of "wherein an edge portion of the capacitor upper electrode is formed onto the protective insulating film," set forth in column 15, lines 11-12 has been deleted as such recitation unduly narrowed the scope of the issued claim based on the prior art.

Further, the recitation of the word "direct" before "connection" in column 15, line 19 has likewise been deleted as such recitation unduly narrowed the scope of the issued claim based upon the prior art.

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith.

Name(s) Registration Number
Donald R. Studebaker 32,815

Correspondence Address: Direct all communications about the application to:

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22204

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Label Here

OR

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Individual
Name

Address

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City

State

Zip

Country

Telephone

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

Hiroki Naito

Full name of person signing (given name, family name)

Director, IP Development Center

Authorized Signing Officer

Signature

Date

Feb. 18, 2008

Address of Assignee:

Matsushita Electric Industrial Co., Ltd.

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